

## EL979950106

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No
Filing Date June 4, 2001
Inventor Klaus F. Schuegraf et al.
Assignee Micron Technology, Inc.
Group Art Unit
Examiner E. Ortiz
Attorney's Docket No
Title: Methods for Forming Wordlines, Transistor Gates, and Conductive
Interconnects, and Wordline, Transistor Gate, and Conductive Interconnect
Structures

## **RESPONSE TO AUGUST 12, 2003 OFFICE ACTION**

To:

Mail Stop Fee Amendment

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

From:

D. Brent Kenady

Tel. 509-624-4276; Fax 509-838-3424

Wells St. John P.S.

601 West First Avenue, Suite 1300

Spokane, WA 99201-3828

Responsive to the Final Office Action dated August 12, 2003, Applicant amends and remarks as follows:

## **AMENDMENTS**

Underlines indicate insertions and strikeouts indicate deletions.

12/17/2003 MAHMED1 00000079 09875501

03 FC:1202

36.00 OP

A27312121013N